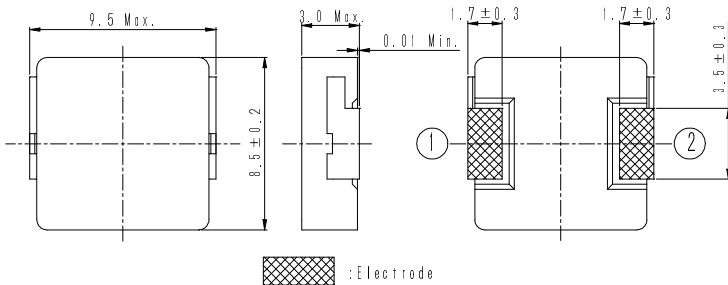


SMD Power Inductor

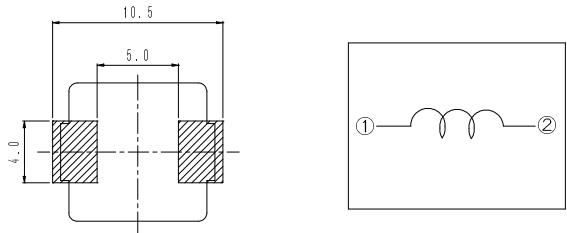
CDMC8D28



Dimension - [mm]



Land pattern and Schematics - [mm]



Electrical Characteristics

Part No.	Stamp	Inductance (μ H) [Within] $\ddagger 1$	D.C.R. (m Ω) [Max.] (Typ.) (at 20°C)	Saturation current (A) (at 20°C) $\ddagger 2$	Temperature rise current (A) $\ddagger 3$
CDMC8D28NP-R18MC	R18	0.18 ± 20%	1.6(1.33)	28.7(35.9)	25.8
CDMC8D28NP-R39MC	R39	0.39 ± 20%	2.8(2.32)	19.8(24.7)	19.9
CDMC8D28NP-R68MC	R68	0.68 ± 20%	4.6(3.84)	16.0(20.0)	15.4
CDMC8D28NP-1R2MC	1R2	1.2 ± 20%	7.0(5.80)	12.2(15.3)	12.9
CDMC8D28NP-1R8MC	1R8	1.8 ± 20%	12.6(10.5)	9.4(11.8)	9.3
CDMC8D28NP-2R2MC	2R2	2.2 ± 20%	16.1(13.4)	8.8(11.0)	8.1
CDMC8D28NP-3R3MC	3R3	3.3 ± 20%	22.2(18.5)	7.0(8.8)	7.3
CDMC8D28NP-3R9MC	3R9	3.9 ± 20%	24.1(20.1)	6.4(8.0)	7.1

$\ddagger 1$. Measuring condition: at 1MHz.

$\ddagger 2$. Saturation current: The value of D.C. current when the inductance decreases to 80% of its nominal value.

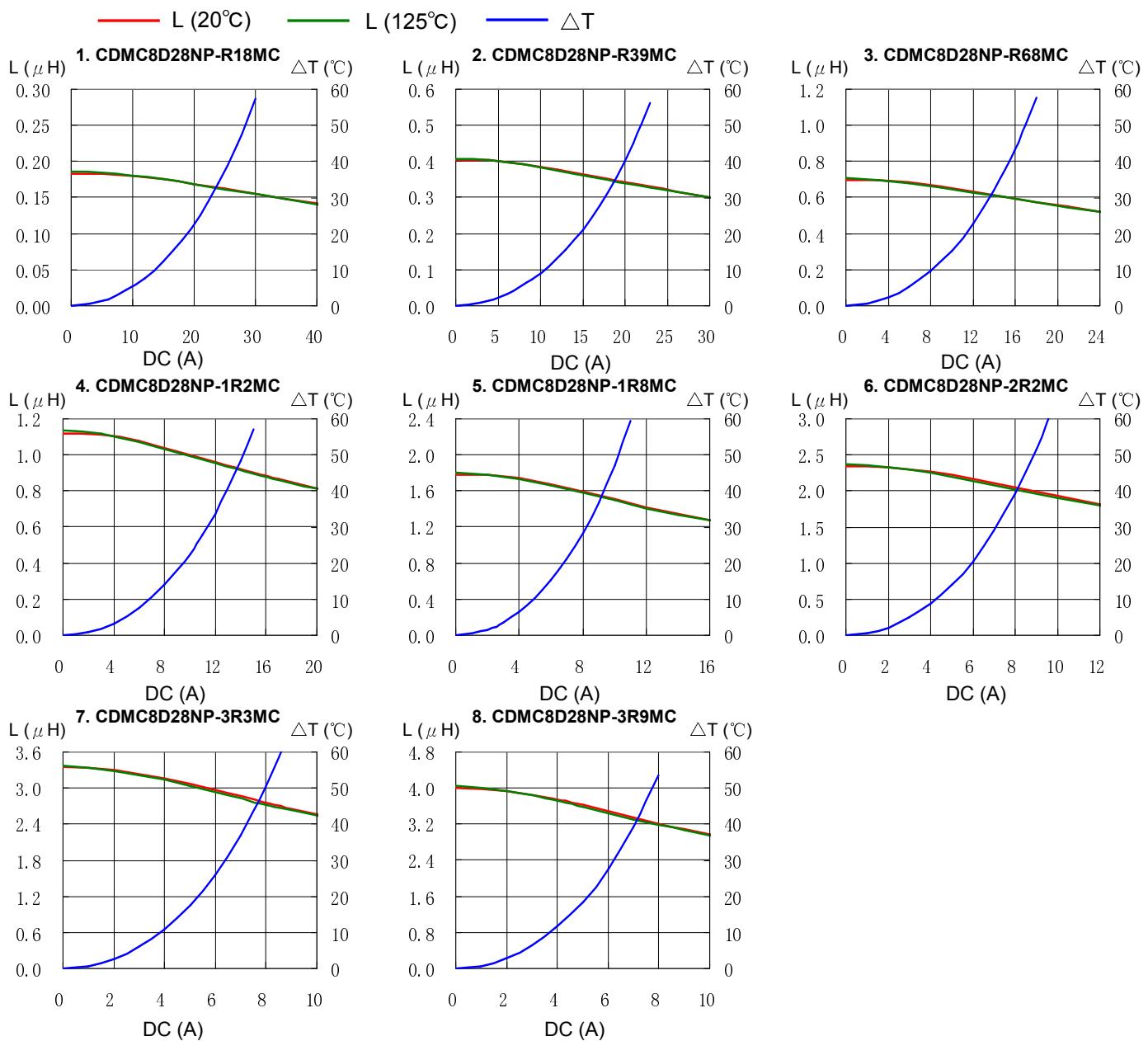
$\ddagger 3$. Temperature rise current: The value of D.C. current when the temperature rise is $\Delta t=40^\circ\text{C}$ ($T_a=20^\circ\text{C}$).

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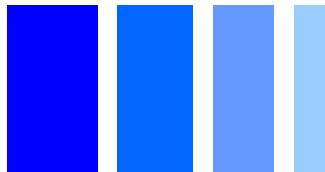


Saturation Current & Temperature Rise Graph



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 **sumida**

Solder Reflow Condition

